



DATASHEET APSYN140 Specification V1.11

100 kHz to 40 GHz Wideband Synthesizer



Document size:

1 title page
11 content pages

DEFINITIONS

- The specifications in the following pages describe the warranted performance of the instrument for 23 ± 5 °C after a 30-minute warm-up period

Typical: Expected mean values, not warranted performance

Min and max: Parameter range that is guaranteed by product design, and/or production tested. Warranted performance specifications include guard-bands to account for the expected statistical performance distribution, measurement uncertainties, and changes in performance due to environmental conditions.

INTRODUCTION

- **Ultra-compact, fast and ultra-low phase noise Frequency Synthesizer with USB & LAN interface**

The APSYN140 is a wideband low phase-noise synthesizer settable from 100 kHz to 40 GHz. The settable output power range is from -5 to +25 dBm.

The module has a milli-Hz frequency resolution and uses a high-stability internal reference. The internal reference can be phase-locked to a user-settable external reference. For highest phase coherence, multiple APSYN140 can be cascaded with just one master reference clock.

The APSYN140 offers dedicated sweeping capabilities with switching speeds of only 500 μ s (20 μ s with option FS) and internal phase and narrow pulse modulation.

The module has USB and LAN interfaces and can be controlled using the SCPI 1999 command set. Operated with an external 24V DC supply, it consumes less than 25 watts.

FACTS, FIGURES & SPECIFICATIONS

Signal Specifications

PARAMETER	MIN	TYPICAL	MAX	NOTE
Frequency Range	100 kHz		40 GHz	Settable from 9 kHz to 43.5 GHz
Resolution		0.001 Hz		
Phase Resolution		0.01 deg		
Switching Speed		1.5 ms		after SCPI command received
CW Mode		500 μs		
Sweep / List Mode		500 μs		
SSB Phase noise at 1 GHz				Option FS (see also plot)
at 1 kHz from carrier		-140 dBc/Hz		
at 100 kHz from carrier		-150 dBc/Hz		
Wideband noise		-160 dBc/Hz		
SSB Phase noise at 10 GHz				
at 1 kHz from carrier		-120 dBc/Hz		
at 100 kHz from carrier		-130 dBc/Hz		
Wideband noise		-160 dBc/Hz		
Output power level				(see also plot)
10 MHz to 1.2 GHz	0 dBm		+20 dBm	
1.2 GHz to 20 GHz	-10 dBm		+20 dBm	
20 to 30 GHz	10 dBm		+20 dBm	
30 to 40 GHz	0 dBm		+18 dBm	
Power Resolution		0.5 dB		
Power Accuracy		±0.5 dB	±2 dB	
Reverse Power Protection				
DC Voltage		7 V		
RF Power			20 dBm	
Output impedance		50 Ohms		
VSWR		1.8		
Spectral purity				
Output harmonics		-15 dBc		
Sub-harmonics		-75 dBc	-45 dBc	< 20 GHz
		-50 dBc	-30 dBc	>20 GHz
Non-harmonic spurious		-75 dBc	-60 dBc	

Modulation Capabilities

PARAMETER	MIN	TYPICAL	MAX	NOTE
Pulse Modulation				
Modulation source		Internal/ External		
Pulse rise/fall time		10 ns		
On/off ratio		40 dB		Pout > +10 dBm, see plot
Pulse overshoot			10%	
Pulse delay		20 ns		
Pulse polarity		Normal, inverse		selectable
External input threshold	0.85 V	0.9 V	0.95 V	TTL compatible
External input voltage range	-0.5 V		+5.5 V	TTL compatible
External input hysteresis		60 mV		
Internal pulse generator				
Repetition frequency (PRF)	0.1 Hz		100 MHz	= 1/T
Duty cycle	1 % to 99 % in 1% steps			within specified minimum pulse width
Pulse width settling range	30 ns		5 s	
Pulse Pattern Modulation & Staggered PRF				Using internal pattern generator
Programmable pattern length	2		65536	
Duty cycle	0.05%		99.95%	
Pulse period (T) accuracy		0.00005xT+ 3ns		
Pulse width accuracy		0.00005xT+ 5ns		
Pulse width resolution		5 ns		
Pulse jitter		2 ns	5 ns	
Polarity		selectable		
Frequency Modulation				
Modulation source		Internal		
Maximum Frequency deviation (peak)		N · 400 MHz		< 1.25 GHz (N=1) 1.25 GHz to 2.5 GHz (N=0.125) 2.5 GHz to 5 GHz (N=0.25) 5 GHz to 10 GHz (N=0.5) 10 GHz to 20 GHz (N=1) 20 GHz to 40 GHz (N=2)
Deviation accuracy		0.50%	2%	
Distortion (THD)		< 1 %		1 kHz rate, 10 kHz deviation
Modulation rate	0.1 Hz		80 kHz	
Modulation waveforms	Sine			
Phase Modulation				
Modulation source		Internal		
Phase deviation (peak)	0		300 · N · rad	
Deviation accuracy		0.50%	2%	
Modulation rate	0.1 Hz		80 kHz	
Modulation waveforms		Sine		
Distortion (THD)		< 1%		1 kHz rate & N x rad deviation

Sweeping Capability, Sweep type: linear, logarithmic, random

PARAMETER	MIN	TYPICAL	MAX	NOTE
Frequency Sweep				
Step time (t_{step})	500 μ s 20 μ s			Option FS
Dwell time (t_{dwell})	15 μ s			

Frequency Reference

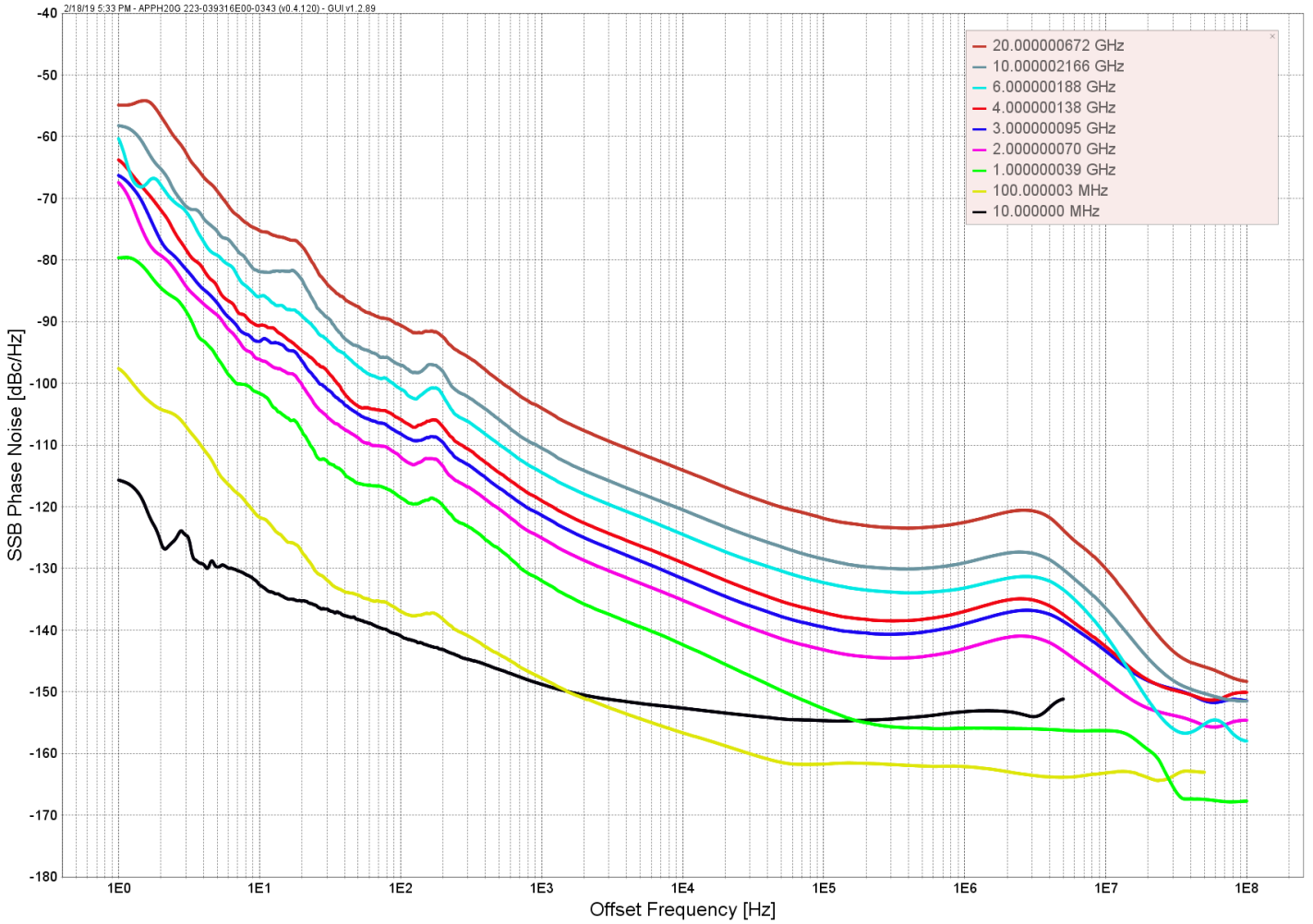
PARAMETER	MIN	TYPICAL	MAX	NOTE
Internal reference frequency		100 MHz 10 MHz		Option LN
Internal Reference Output Frequency				
Temperature stability			\pm 100 ppb \pm 20 ppb	0 to 50 degC Option LN
Aging 1st year			1 ppm 0.03 ppm	Option LN
Aging per day			5 ppb 0.5 ppb	after 30 days operations Option LN
Warm-up time		5 min		
Output of internal reference		100 MHz		
		10/100 MHz		Option LN
Output power	0 dBm	5 dBm		
Output impedance		50 Ohms		
Bypass Internal reference Input		100 MHz		High phase synchronous mode
Phase Lock to External Reference	1 MHz	10 MHz integer MHz	250 MHz	Option VREF
Reference Bypass Mode		100 MHz		
Reference input level				
10 MHz or 1-250 MHz	-5 dBm	0 dBm	+13 dBm	
Bypass 100 MHz	5 dBm		+15 dBm	
Reference input impedance		50 Ohms		
Lock Range				
10 MHz or 1-250 MHz			\pm 1.5 ppm	
Bypass 100 MHz			>100 ppm	

Trigger (TRIG IN): Input is TRIG IN at front panel

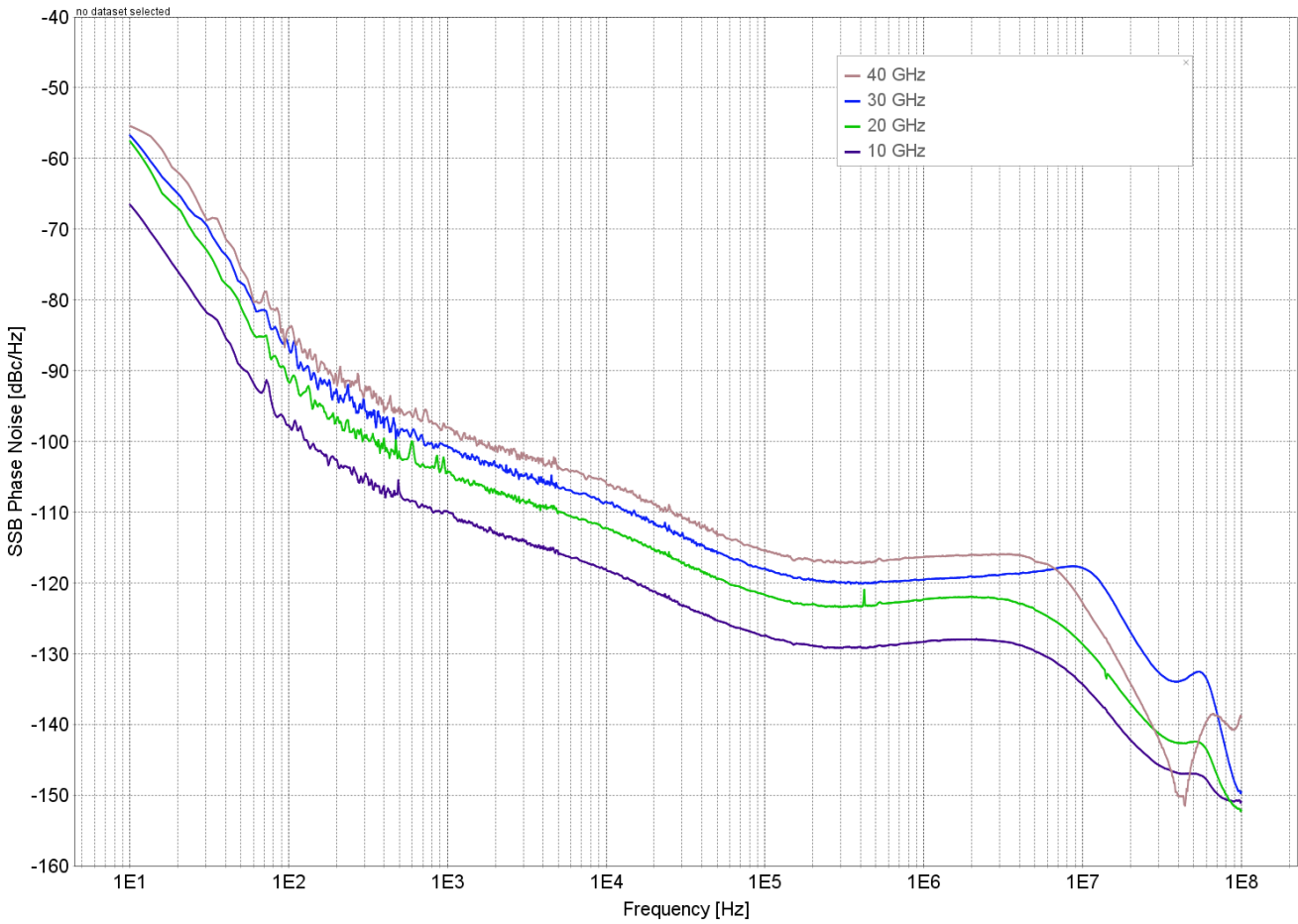
PARAMETER	MIN	TYPICAL	MAX	NOTE
Trigger Types	Continuous, single (point), gated, gated direction			
Trigger Source	external, bus (LAN, USB)			
Trigger Modes	Continuous free run, trigger and run, reset and run			
Trigger latency		5 ns		
Trigger uncertainty		10 ns		
External Trigger delay	50 ns		40 s	
External Delay Resolution		5 ns		
Trigger Modulo	1		255	Execute only on Nth trigger event
Trigger Polarity	Rising, falling			
External trigger input threshold	0.85 V	0.9 V	0.95 V	TTL compatible
External trigger input voltage range	-0.5 V		+5.5 V	TTL compatible
External trigger input hysteresis		60 mV		

TYPICAL PERFORMANCE CURVES

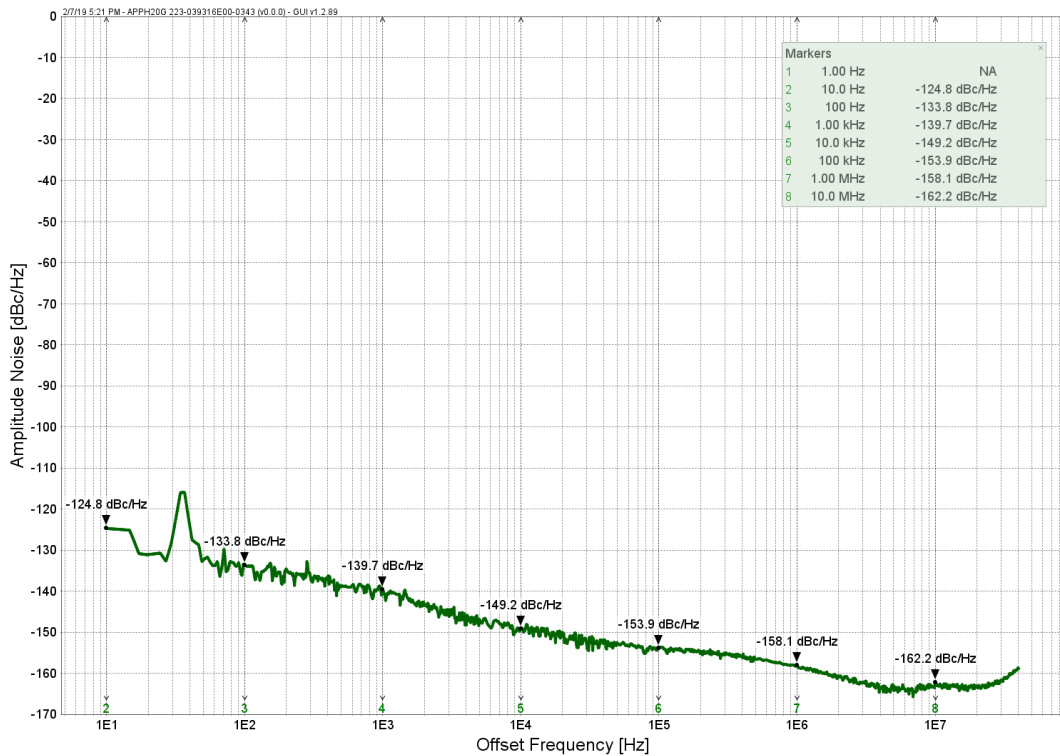
Phase Noise Performance with option LN



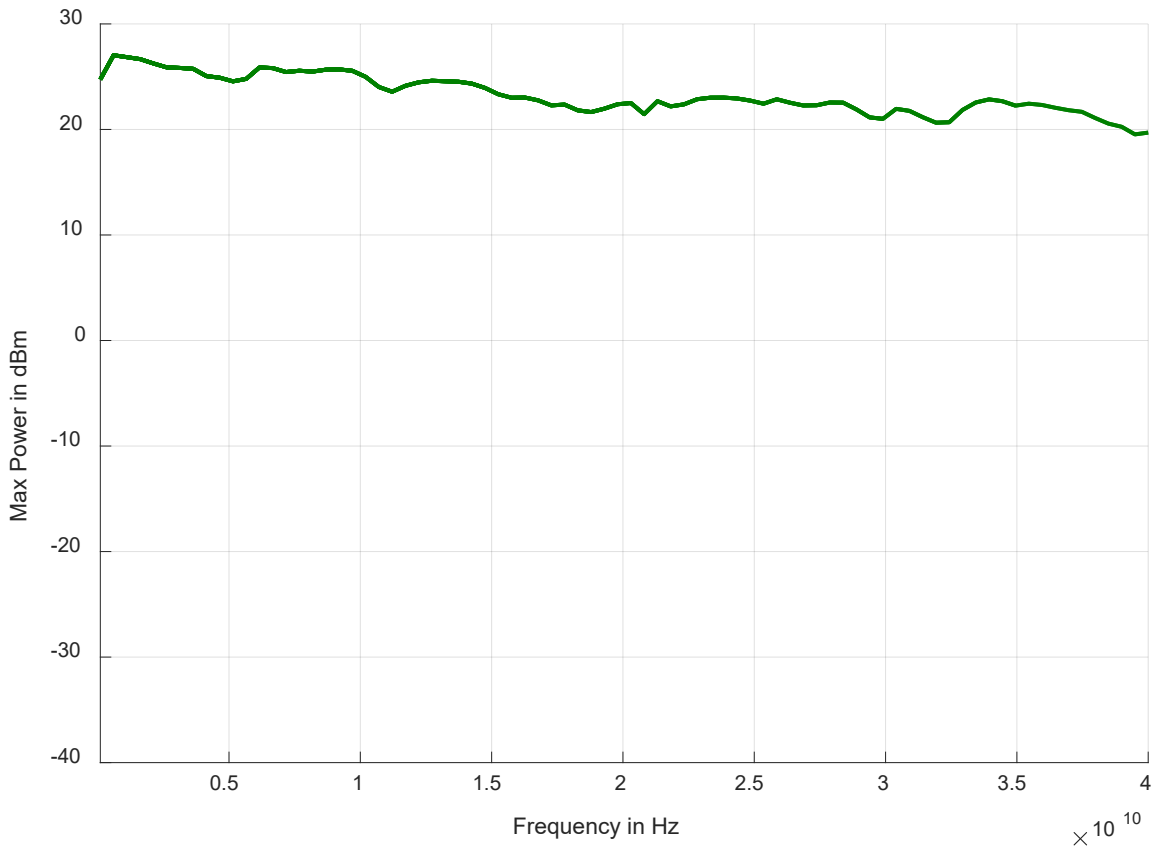
Phase Noise Performance without option LN (10, 20, 30, 40 GHz)



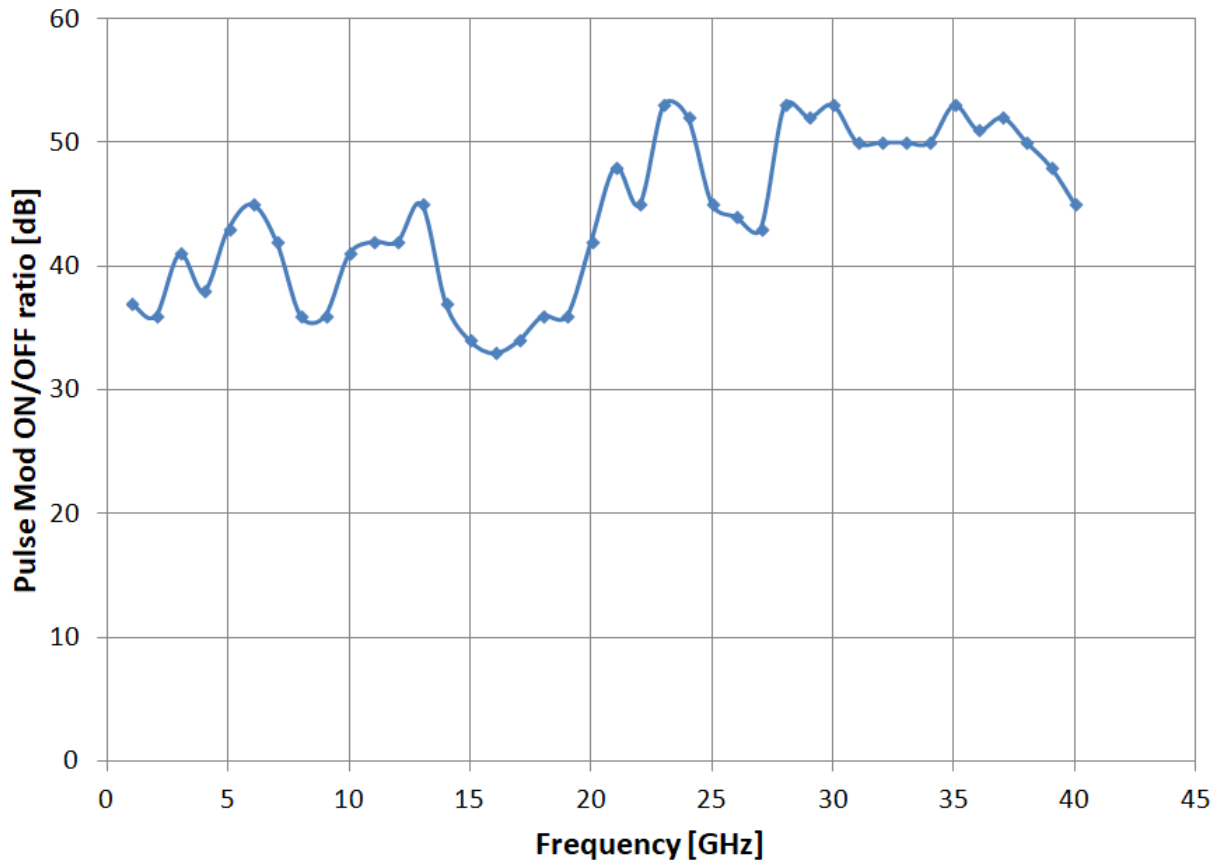
Amplitude Noise at 10 GHz



Maximum Output Power



Pulse Modulation on-off ratio



ORDERING INFORMATION



HOST MODEL	PRODUCT	DESCRIPTION
APSYN140	APSYN140	300 kHz to 40 GHz Synthesizer, flange-mount module
APSYN140	Option LN	Enhanced close in phase noise & frequency stability
APSYN140	Option FS	Ultra-fast switching speed
APSYN140	Option VREF	Variable external reference

GENERAL CHARACTERISTICS

Remote programming interfaces

Ethernet 100BaseT LAN interface, USB 2.0, Control language SCPI Version 1999.0

Power requirements 24V ± 3.0 VDC; 25 W maximum

Mains adapter supplied: 100-240 VAC in/ 24 V 4.0 A DC out

Environmental (Levels similar to MIL-PRF-28800F Class 3/4)

Operating temperature range 0 to 45 °C

Storage temperature range -40 to 70 °C

Operating and storage altitude up to 15,000 feet (4600 m)



Safety/EMC complies with applicable Safety and EMC regulations and directives.

Weight ≤ 1.0 kg (2.2 lbs) net

Dimensions 27 x 10.5 x 6 cm [10.63 x 4.13 x 2.36 in]



Front view



Rear view

Document History

Version/Status	Date	Author	Notes
V10	2018-05-01	jk	first release
V101	2018-08-13	jk	
V102	2018-11-27	jk	Power ranges, Pictures, Ordering Info
V103	2018-12-10	mm	Introduction text
V1031	2019-01-28	db	Output of internal reference: 100 MHz
V104	2019-03-18	jk	Corrections
V110	2020-09-15	jk	Max power plot
V111	2021-02-25	db	Pulse and trigger input electrical specifications

AnaPico Inc. of Switzerland

Europastrasse 9
8152 Glattbrugg
Switzerland

Phone +41 44 440 00 50
Email sales@anapico.com

www.anapico.com
www.anapico.com/downloads/

